POZNAN UNIVERSITY OF TECHNOLOGY



EUROPEAN CREDIT TRANSFER AND ACCUMULATION SYSTEM (ECTS)

COURSE DESCRIPTION CARD - SYLLABUS

Course name Embedded systems [S1EiT1>SWB]

Course				
Field of study Electronics and Telecommunications		Year/Semester 4/7		
Area of study (specialization)		Profile of study general academ	ic	
Level of study first-cycle		Course offered i polish	n	
Form of study full-time		Requirements elective		
Number of hours				
Lecture 15	Laboratory class 15	ses	Other (e.g. online) 0	
Tutorials 0	Projects/semina 0	Irs		
Number of credit points 3,00				
Coordinators	Lecturers			
dr hab. inż. Olgierd Stankiewic olgierd.stankiewicz@put.pozna				
 Droroquisitos				

Prerequisites

none

Course objective

none

Course-related learning outcomes

none

Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows: none

Programme content

none

Teaching methods

none

Bibliography

none

Breakdown of average student's workload

	Hours	ECTS
Total workload	75	3,00
Classes requiring direct contact with the teacher	31	2,00
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	44	1,00